

Digital Input Board with Opto-Isolation for PCI Express

DI-128L-PE



* Specifications, color and design of the products are subject to change without notice.

Features

Optocoupler isolated inputs (compatible with current sink output)

This product has the 128 channels of Optocoupler isolated input (compatible with current sink output) whose response speed is 200µsec. Common terminal provided per 16 channels, capable of supporting a different external power supply. Supporting driver voltages of 12 - 24 VDC for I/O.

Optocoupler bus isolation

As the PC is isolated from the input and output interfaces by Optocoupler, this product has excellent noise performance.

You can use 16 input signals as interrupt request signals. You can use 16 input signals as interrupt request signals and also disable or enable the interrupt in bit units and select the edge of the input signals, at which to generate an interrupt.

Windows/Linux compatible driver libraries are attached.

Using the attached driver library API-PAC(W32) makes it possible to create applications of Window/Linux. In addition, a diagnostic program by which the operations of hardware can be checked is provided.

This product has a digital filter to prevent wrong recognition of input signals from carrying noise or a chattering.

This product has a digital filter to prevent wrong recognition of input signals from carrying noise or a chattering. All input terminals can be added a digital filter, and the setting can be performed by software.

Functions and connectors are compatible with PCI compatible board PI-128L(PCI)H.

The functions same with PCI compatible board PI-128L(PCI)H are provided.

In addition, as there is compatibility in terms of connector shape and pin assignments, it is easy to migrate from the existing system.

LabVIEW is supported by a plug-in of dedicated library VI-DAO.

Using the dedicated library VI-DAQ makes it possible to make a LabVIEW application.

This product is a PCI Express bus-compliant interface board used to provide a digital signal input function on a PC. This product can input digital signals at 12 - 24VDC.

This product features 128 Optocoupler isolated inputs. You can use 16 input signals as interrupt inputs. In addition, the digital filter function to prevent wrong recognition of input signals is provided.

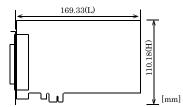
Windows/Linux driver is bundled with this product. Possible to be used as a data recording device for LabVIEW, with dedicated libraries.

Specification

	Specification										
Input											
Input format	Optocoupler isolated input (Compatible with current sink output)(Negative logic *1)										
Number of input	128 channels (16 channels available for interrupts) (1 common per 16										
signal channels	channels unit)										
Input resistance	4.7kΩ										
Input ON current	2.0mA or more										
Input OFF current	0.16mA or less										
Interrupt	16 interrupt input signals are arranged into a single output of interrupt signal INTA. An interrupt is generated at the rising edge (HIGH-to-LOW transition) or falling edge (LOW-to-HIGH transition) (set by software).										
Response time	Within 200μsec										
Common											
Allowable distance of signal extension	Approx. 50m (depending on wiring environment)										
I/O address	Any 32-byte boundary										
Interruption level	1 level use										
Max. board count for connection	16 boards including the master board										
Isolated Power	250Vrms										
External circuit power supply	12 - 24VDC(±10%)										
Power consumption (Max.)	3.3VDC 600mA										
Operating condition	0 - 50°C, 10 - 90%RH (No condensation)										
Bus specification	PCI Express Base Specification Rev. 1.0a x1										
Dimension (mm)	169.33(L) x 110.18(H)										
Connector	100 pin 0.8mm pitch connector [F (female) type] x 2 HDRA-E100W1LFDT1EC-SL+[HONDA TSUSHIN KOGYO CO., LTD.] or equivalent to it										
Weight	215g										

^{*1} Data "0" corresponds at the High level and data "1" correspond at the Low level.

Board Dimensions



The standard outside dimension (L) is the distance from the end of the board to the outer surface of the slot cover.



Support Software

Windows version of digital I/O driver API-DIO(WDM) / API-DIO(98/PC)

[Stored on the bundled CD-ROM driver library API-PAC(W32)]

The API-DIO(WDM) / API-DIO(98/PC) is the Windows version driver library software that provides products in the form of Win32 API functions (DLL). Various sample programs such as Visual Basic and Visual C++, etc and diagnostic program useful for checking operation is provided.

< Operating environment >

Windows Vista, XP, Server 2003, 2000

Adaptation language Visual Basic, Visual C++, Visual C#,

Delphi, C++ Builder

You can download the updated version from the CONTEC's Web site (http://www.contec.com/apipac/). For more details on the supported OS, applicable language and new information, please visit the CONTEC's Web site.

Linux version of digital I/O driver API-DIO(LNX) [Stored on the bundled CD-ROM driver library API-PAC(W32)]

The API-DIO(LNX) is the Linux version driver software which provides device drivers (modules) by shared library and kernel version. Various sample programs of gcc are provided.

< Operating environment >

RedHatLinux, TurboLinux OS

(For details on supported distributions, refer to Help available after installation.)

Adaptation language acc

You can download the updated version from the CONTEC's Web site (http://www.contec.com/apipac/). For more details on the supported OS, applicable language and new information, please visit the CONTEC's Web site.

Data acquisition VI library for LabVIEW VI-DAQ (Available for downloading (free of charge) from the CONTEC web site.)

This is a VI library to use in National Instruments LabVIEW. VI-DAQ is created with a function form similar to that of LabVIEW's Data Acquisition VI, allowing you to use various devices without complicated settings.

See http://www.contec.com/vidaq/ for details and download of VI-DAQ.

Cable & Connector

Cable (Option)

Shielded Cable With Two 100pin Connector

: PCB100PS-0.5 (0.5m) : PCB100PS-1.5 (1.5m) : PCB100PS-3 (3m)

: PCB100PS-5 (5m)

Connection Conversion Shield Cable (100P→96P)

: PCB100/96PS-1.5 (1.5m) : PCB100/96PS-3 (3m) : PCB100/96PS-5 (5m)

Flat Cable with One 100-Pin Connector

: PCA100P-1.5 (1.5m) : PCA100P-3 (3m) : PCA100P-5 (5m)

Connection Conversion Shield Cable (100P→37P D-SUB x 2)

: PCB100WS-1.5 (1.5m) : PCB100WS-3 (3m) : PCB100WS-5 (5m)

Accessories

Accessories (Option)

Screw Terminal Unit (M3 x 100P) : EPD-100A *1*4*6 Screw Terminal Unit (M3 x 96P) : EPD-96A *2*4*6 Screw Terminal Unit (M3.5 x 96P) : EPD-96 *2*4 : DTP-64(PC) *2*4 Terminal Unit for Cables (M2.5 x 96P)

Connector Conversion Board

(96-Pin→37-Pin x 2) : CCB-96 *2*4 Signal Monitor / Output Accessory

for Digital I/O (64P) : CM-64(PC)E *2*4 Screw Terminal Unit (M3 x 37P) : EPD-37A *3*5*6 : EPD-37 *3*5 Screw Terminal Unit (M3.5 x 37P) : DTP-3A *3*5 General Purpose Terminal (M3 x 37P) Screw Terminal (M2.6 x 37P) : DTP-4A *3*5

- PCB100PS optional cable is required separately.
- PCB100/96PS optional cable is required separately PCB100WS optional cable is required separately.
- If using both the CNA and CNB connectors, two each of the terminal block and cable sets are required.
- *5 If using both the CNA and CNB connectors, two cable sets are required.
- You will also require sufficient terminal blocks for the number of I/O points you are using. *6
- "Spring-up" type terminal is used to prevent terminal screws from falling off. Check the CONTEC's Web site for more information on these options.

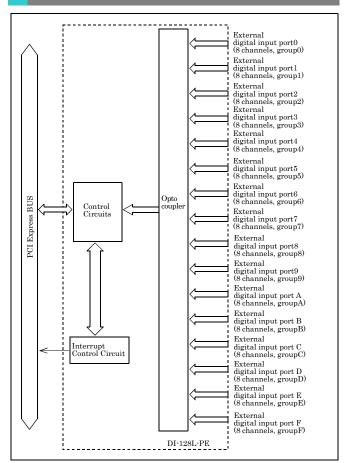
Packing List

Board [DI-128L-PE] ...1 First step guide ... 1

CD-ROM *1 [API-PAC(W32)] ...1

The CD-ROM contains the driver software and User's Guide.

Block Diagram



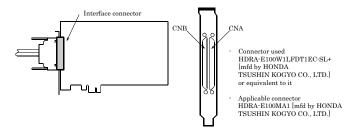
If using both the CNA and CNB connectors, two cable sets are required.



How to connect the connectors

Connector shape

The on-board interface connector (CNA, CNB) is used when connecting this product and the external devices.



Please refer to page 2 for more information on the supported cable and accessories.

Connector Pin Assignment

Pin Assignments Interface Connector (CNA, CNB)

L 111 1			ши	CIIL	> 111	ile	illa	ce c	U	illec	LOI	"	CNA, C	IND	,		
Common	P-E/F	100				50	P-A/B	Common	1 '		N.C.	1			51	N.C.	
plus pin								plus pin					1	F			
for								for									
+E/+F	P-E/F	99				49	P-A/B	+A/+B			N.C.	2			52	N.C.	
input								input									
ports								ports						L			
	I-F7	98				48	I-B7				N.C.	3		L	53	N.C.	
	I-F6	97				47	I-B6				N.C.	4		L	54	N.C.	
	I-F5	96				46	I-B5				N.C.	5	l	Ļ	55	N.C.	
+F port	I-F4	95				45	I-B4	+B port			N.C.	6		L	56	N.C.	
(Input)	I-F3	94				44	I-B3	(Input)			* I-00	7	l	Ļ	57	I-40	
	I-F2	93				43	I-B2				* I-01	8	l	Ļ	58	I-41	
	I-F1	92				42	I-B1				* I-02	9	l	Ļ	59	1-42	
	I-F0	91		CNB	1	41	I-B0			+0 port	* I-03	10	CNA	L.	60	1-43	+4 port
	I-E7	90	100	$\overline{}$	-50	40	I-A7			(Input)	* I-04	11		-51	61	1-44	(Input)
	I-E6	89	1	1	1	39	I-A6				* I-05	12	1 11 11	L.	62	I-45	
	I-E5	88				38	I-A5				* I-06	13		L.	63	I-46	
+E port	I-E4	87				37	I-A4	+A port			* I-07	14		F	64	1-47	
(Input)	I-E3	86				36	I-A3	(Input)	1		* I-10	15	1	Ļ	65	I-50	
	I-E2	85				35	I-A2		1		* I-11	16	1	-	66	I-51	
	I-E1	84				34	I-A1		1		* I-12	17	1	ŀ	67	1-52	_
	I-E0	83				33	I-A0		1	+1 port	* I-13	18	1	ŀ	68	1-53	+5 port
	N.C.	82				32	N.C.		1	(Input)	* I-14	19	1	-	69	1-54	(Input)
-	N.C.	81				31	N.C.		ł		* I-15	20	1		70 71	1-55	
									1				1	ŀ		1-56	
-	N.C.	79 78				29 28	N.C.		ł	Common	* I-17 P-0/1	22	1		72 73	I-57 P-4/5	Common
	N.C.	78				28	N.C.		1	plus pin	P=0/1	23	1	ŀ	13	P-4/5	plus pin
										for							for
	N.C.	77				27	N.C.			+0/+1	P-0/1	24			74	P-4/5	+4/+5
									ı	input ports]	L			input ports
	N.C.	76				26	N.C.		1		N.C.	25		L	75	N.C.	
	N.C.	75				25	N.C.		ı		N.C.	26]	L	76	N.C.	
Common	P-C/D	74				24	P-8/9	Common			N.C.	27		L	77	N.C.	
plus pin for								plus pin									
+C/+D	P-C/D	73				23	P-8/9	for +8/+9			N.C.	28			78	N.C.	
input	F-C/D	13				23	F=0/9	input			IV.C.	20			10	IV.C.	
ports								ports									
	I-D7	72				22	1-97		1		N.C.	29	1	Ī	79	N.C.	
	I-D6	71				21	I-96				N.C.	30	1	Ī	80	N.C.	
	I-D5	70				20	I-95				N.C.	31		F	81	N.C.	
+D port	I-D4	69				19	I-94	+9 port			N.C.	32]		82	N.C.	
(Input)	I-D3	68				18	I-93	(Input)			I-20	33]		83	1-60	
	I-D2	67				17	I-92				I-21	34]		84	I-61	
	I-D1	66				16	I-91				I-22	35]	L	85	I-62	
	I-D0	65				15	I-90		1	+2 Port	I-23	36		L	86	I-63	+6 port
	I-C7	64				14	I-87			(Input)	I-24	37]	L	87	I-64	(Input)
	I-C6	63	51	⌴	_1	13	I-86				I-25	38	50	_100	88	I-65	
	I-C5	62)	12	I-85				I-26	39	```	100	89	I-66	
+C port	I-C4	61				11	I-84	+8 port			I-27	40	1		90	I-67	
(Input)	I-C3	60				10	I-83	(Input)	1		I-30	41	l		91	1-70	
	I-C2	59				9	I-82		1		I-31	42	l	L	92	I-71	
	I-C1	58				8	I-81		1		I-32	43	l	Ļ	93	1-72	
	I-C0	57				7	I-80		1	+3 Port	I-33	44	l	L	94	1-73	+7 Port
	N.C.	56				6	N.C.		1	(Input)	I-34	45	l	L	95	1-74	(Input)
	N.C.	55				5	N.C.		1		I-35	46	l	L	96	1-75	
	N.C.	54				4	N.C.		1		I-36	47		L	97	I-76	
	N.C.	53				3	N.C.		1		I-37	48	l	-	98	1-77	
	N.C.	52				2	N.C.		1	Common	P-2/3	49	l	L	99	P-6/7	Common
	N.C	51				1	N.C		1	plus pin for +2/+3	P-2/3	50	l		100	P-6/7	plus pin for +6/+7
	N.C.	51				1	N.C.		1	input ports	P-2/3	οU	l		100	P-0//	input ports
				_		_			Ι.	put porto							put ports

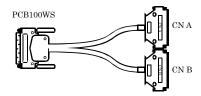
* I-00 - I-17 can be used as interrupt signal.

	128 channels input signal. Connect output signals from the external device to these pins.
P-0/1 - P-E/F	Connect the positive side of the external power supply. These pins are common to 16 channels input signal.
N.C.	This pin is left unconnected.

Pin assignments for connecting to the PCB100/96PS or PCB100WS



Unconnected	N.C.	B01 B02		A01 A02	N.C.	Unconnected	Unconnected	N.C.	B01 B02		A01 A02	N.C.	Unconnected
	I-C0	B03		A03	1-80			1-40	B03		A03	1-00	
_	I-C1	B04		A04	I-81			1-41	B04		A04	I-01	
	I-C2	B05		A05				1-42	B05		A05	1-02	
+C port (Input)	I-C3	B06		A06	I-83	+8 port	+4 port	I-43	B06		A06	I-03	+0 port (Input)
(Input)	I-C4	B07		A07	I-84	(Input)	(Input)	1-44	B07		A07	I-04	
	I-C5	B08	l	80A	I-85			1-45	B08		80A	1-05	
	I-C6	B09		A09	I-86			I-46	B09		A09	I-06	
	I-C7	B10		A10	1-87			1-47	B10		A10	1-07	
	I-D0	B11		A11	1-90			1-50	B11		A11	I-10	
	I-D1	B12		A12	I-91			I-51	B12		A12	I-11	
_	I-D2	B13		A13	1-92		l _	1-52	B13		A13	I-12	
+D port	I-D3	B14		A14	1-93	+9 port	+5 port	I-53	B14		A14	I-13	+1 port
(Input)	I-D4	B15		A15	I-94	(Input)	(Input)	I-54	B15		A15	I-14	(Input)
	I-D5	B16	For connecting the board CNB	A16	I-95			I-55	B16	For connecting the board CNA	A16	I-15	
	I-D6	B17	$\overline{}$	A17	1-96			1-56	B17		A17	I-16	l
	I-D7	B18	Box Bid Laid Ans	A18	I-97			I-57	B18	Box Dell [48] Ann	A18	I-17	
Common plus pin	P-C/D	B19		A19	P-8/9	Common plus pin	Common	P-4/5	B19		A19	P-0/1	Common
for +C/+D				l	l	for +8/+9	plus pin		L	11 11			plus pin
output	P-C/D	B20		A20	P-8/9	output	for +4/+5 input ports	P-4/5	B20		A20	P-0/1	for +0/+1 input ports
ports						ports	input ports						input pons
	N.C.	B21		A21	N.C.		Unconnected	N.C.	B21		A21	N.C.	
	N.C.	B22		A22	N.C.	Unconnected		N.C.	B22		A22	N.C.	
	N.C.	B23		A23	N.C.			N.C.	B23		A23	N.C.	
	N.C.	B24		A24	N.C.			N.C.	B24		A24	N.C.	
Unconnected	N.C.	B25		A25	N.C.			N.C.	B25		A25	N.C.	Unconnecte
	N.C.	B26		A26	N.C.			N.C.	B26		A26	N.C.	
	N.C.	B27		A27	N.C.			N.C.	B27		A27	N.C.	
	N.C.	B28		A28	N.C.			N.C.	B28		A28	N.C.	
	N.C.	B29		A29	N.C.			N.C.	B29		A29	N.C.	
	N.C.	B30		A30	N.C.			N.C.	B30		A30	N.C.	
	I-E0	B31		A31	I-A0			1-60	B31		A31	1-20	
	I-E1	B32	Res Aus	A32	I-A1		+6 port (Input)	I-61	B32		A32	1-21	
	I-E2	B33		A33	I-A2			I-62	B33	Bast	A33	I-22	
+E port (Input)	I-E3	B34	1400 (11)	A34	I-A3	+A port (Input)		I-63	B34	149[[11]	A34	I-23	+2 port (Input)
(IIIput)	I-E4	B35		A35	I-A4	(Input)		1-64	B35		A35	1-24	(Iliput)
	I-E5	B36		A36	I-A5			I-65	B36		A36	1-25	
	I-E6	B37 B38		A37 A38	I-A6 I-A7			I-66	B37 B38		A37 A38	I-26	
	I-E7	B39		A39	I-A7			1-67	B39		A39	1-27	
	I-F0	B40		A40	I-B0			I-70	B39		A40	I-30	
	I-F1	B41		A40	1-B1	1		1-71	B41		A40	1-31	l
+F port	I-F3	B42		A42	I-B2	+B port	+7 port	1-72	B42		A42	I-32	+3 port
(Input)	I-F4	B43		A43	I-B3	(Input)	(Input)	1-74	B43		A43	I-34	(Input)
(,/	I-F4	B44		A44	1-B4	()	(1-74	B44		A44	1-34	(
	I-F6	B45		A45	I-B6	1 1	1	1-75	B45		A45	I-36	l
	I-F7	B46		A46	I-B7	1		1-77	B46		A46	1-37	l
Common	P-E/F	B47		A47	P-A/B	Common	0		B47		A47	P-2/3	0
plus pin		- "				plus pin	Common plus pin	. 311	- **			. 20	Common plus pin
for +E/+F	P-E/F	B48		A48	P-A/B	for +A/+B	for +6/+7	P-6/7	B48		A48	P-2/3	for +2/+3
output ports	E/F	D46		M40	1MB	output	input ports	10//	D40		A40	-2/3	input ports
						ports							



	N.C.	19						N.C. 19			10
Common plus pin for +8/+9 input ports	P-8/9	18	CNA of PCB100WS connecting to the board CNB	37	P-A/B	Common plus pin for +A/+B input ports	Common plus pin for +0/+1 input ports	P-0/1 18	CNA of PCB100WS connecting to the board CNA	37 P-2/	Common plus pin for +2/+3 input ports
+9 port (Input)	I-96 I-95 I-94 I-93 I-92 I-91	17 16 15 14 13 12 11	8 8 37	36 35 34 33 32 31 30 29	I-B5 I-B4 I-B3 I-B2	+B port (Input)	+1 port (Input)	I-17 17 I-16 16 I-15 15 I-14 14 I-13 13 I-12 12 I-11 11 I-10 10	8 8 37	36 I-37 35 I-36 34 I-35 33 I-34 32 I-33 31 I-32 30 I-31 29 I-30	+3 port (Input)
+8 port (Input)	I-86 I-85 I-84 I-83 I-82	9 7 6 5 4 3 2	1 20	28 27 26 25 24 23 22 21 20	I-A4	+A port (Input)	+0 port (Input)	I-07 9 I-06 8 I-05 7 I-04 6 I-03 5 I-02 4 I-01 3 I-00 2 N.C. 1	1 8 9 20	28 I-27 27 I-26 26 I-25 25 I-24 24 I-23 23 I-22 22 I-21 21 I-20 20 N.C.	+2 port (Input)

	N.C.	19					3	N.C.	19						
Common plus pin for +C/+D input ports	P-C/D	18	19	37	P-E/F	plus	Common plus pin for +4/+5 input ports	P-4/5	18	19	37	P-6/7	Common plus pin for +6/+7 input ports		
+D port (Input)	I-D7 I-D6 I-D5 I-D4 I-D3 I-D2 I-D1 I-D0	13 12 11 10	8 8 37	36 34 33 32 31 30 29	I-F7 I-F6 I-F5 I-F4 I-F3 I-F2 I-F1 I-F0	+F port (Input)	+5 port (Input)	I-57 I-56 I-55 I-54 I-53 I-52 I-51 I-50		8 8 37	36 34 33 32 31 30 29	I-76 I-75 I-74 I-73 I-72 I-71 I-70	+7 port (Input)		
+C port (Input)	I-C7 I-C6 I-C5 I-C4 I-C3 I-C2 I-C1 I-C0 N.C.	9 8 7 6 5 4 3 2	1 8 20	28 27 26 25 24 23 22 21 20	I-E7 I-E6 I-E5 I-E4 I-E3 I-E2 I-E1 I-E0 N.C.	+E port (Input)	+4 port (Input)	I-47 I-46 I-45 I-44 I-43 I-42 I-41 I-40 N.C.	9 7 6 5 4 3 2	1 8 8 20	28 27 26 25 24 23 22 21 20	I-66 I-65 I-64 I-63	+6 port (Input)		

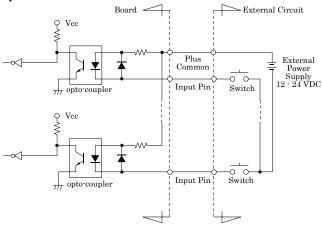


Connecting Input signals

Connect the input signals to a device which can be current-driven, such as a switch or transistor output device. The connection requires an external power supply to feed currents

The board inputs the ON/OFF state of the current-driven device as a digital value.

Input Circuit

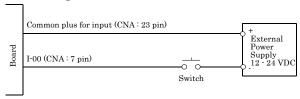


^{*} I-xx shows input pins.

This product input circuit of interface is illustrated in the image above.

The signal inputs are isolated by Optocoupler (ready to accept current sinking output signals). The board therefore requires an external power supply to drive the inputs. The power requirement for each input pin is about 5.1mA at 24VDC (about 2.6mA at 12VDC).

Connecting to the Switch



When the switch is ON, the corresponding bit contains 1. When the switch is OFF, by contrast, the bit contains 0.

Connecting the Sink Type Output and Sink Output Support Input

The following example shows a connection between a sink type output (output board) and a sink output support input (input board). Refer to this connection example when you connect such boards to each other.

